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MAR 18 2005

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Appl. No.: 10/604,102

Applicant(s): Steegan et al.

Filed.: June 26, 2003

Art Unit: 2815

Dkt. No.: FIS920030051US1

Examiner: Eckert II, George C.

Title: **SELECTIVE SILICON-ON-INSULATOR ISOLATION STRUCTURES AND METHOD**

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Honorable Commissioner for Patents  
P. O. Box 1450  
Alexandria, VA 22313-1450

**REQUEST FOR RECONSIDERATION**

This Request for Reconsideration is being filed in response to the Office Action mailed on February 28, 2005.

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